

The Department of Electronics and Communication Engineering of NMIT is organizing a Faculty Development Programme on "**High Performance Embedded System Design using Multi-core/Multi-processor SoCs**" from **20th December - 24th December 2016** under **TEQIP-II**.

This program aims to focus on recent trends in embedded system development and applications. A mix of topics and speakers from the industry and academia is carefully chosen so as to enable the participants, particularly the faculty, get introduced to the aspects of system development using Multi-core / Multi-processor SoCs and their applications. Considering that such SoCs are currently playing a major role in the embedded domain and thus its imminent inclusion in the syllabus as well as use in the student projects, the focus of lecture and demo/hands-on sessions of the program would be on the widely used multi-core/multi-processor SoCs.

For more details refer brochure.

Last date for registration is 18/12/2016

Registration fees

Students/ Researchscholars:		Rs500.00
Faculty	:	Rs1500.00
Industry/Corporate	:	Rs2500.00